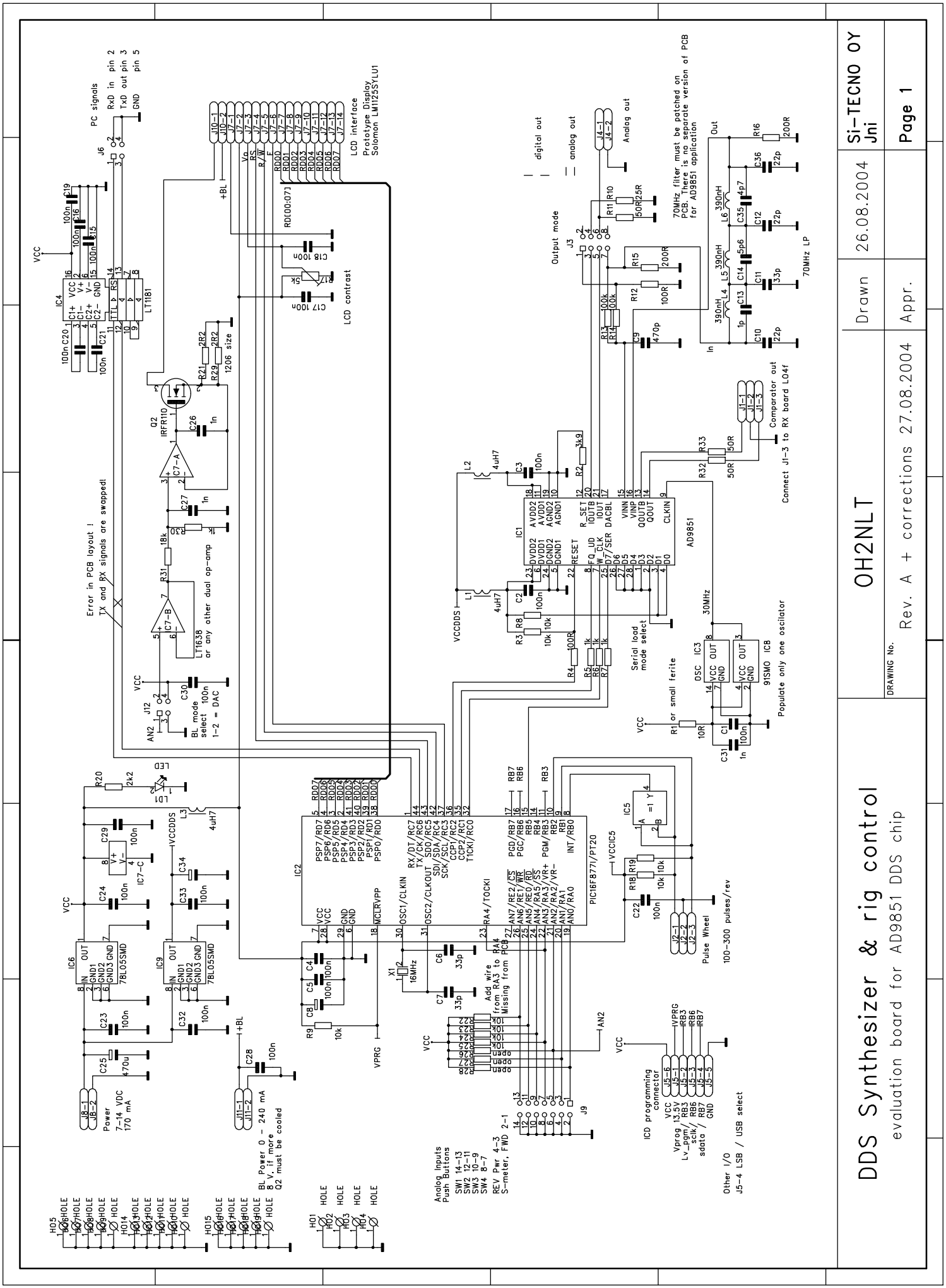
DDS Synthesizer & rig control
evaluation board for AD9851 DDS chip
OH2NLT
Rev. A + corrections 27.08.2004
Si-TECNO OY
Jni
26.08.2004
Page 1
Drawn
Appr.
DRAWING No.
AD9851
PIC16F877/PT20
LCD interface
Prototype Display
Salomon LM1125SYLU1
LCD contrast
Analog out
digital out
analog out
Output mode
70MHz filter must be patched on PCB. There is no separate version of PCB for AD9851 application
70MHz LP
Comparator out
Connect J1-3 to RX board LO4f
Serial load mode select
Populate only one oscilator
Pulse Wheel
100-300 pulses/rev
ICD programming connector
Other I/O
J5-4 LSB / USB select
Analog Inputs
Push Buttons
REV Pwr 4-3
S-meter, FWD 2-1
Add wire from RA3 to RA4
Missing from PCB
Error in PCB layout !
TX and RX signals are swapped!
LT1638
or any other dual op-amp
BL mode select
1-2 = DAC
Power
7-14 VDC
170 mA
BL Power 0 - 240 mA
8 V, if more
Q2 must be cooled
PC signals
RxD in pin 2
TxD out pin 3
GND pin 5
LT1181
IRFR110
78L05SMD
91SMO ICB
DSC IC3
30MHz
16MHz
VCC
VCCDDS
VPRG
+BL
AN2